IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Marco Pasotti et al.
Application No.	:	10/768,743
Filed	:	January 29, 2004
For	:	EMBEDDABLE FLASH MEMORY SYSTEM FOR NON-VOLATILE STORAGE OF CODE, DATA AND BIT-STREAMS FOR EMBEDDED FPGA CONFIGURATIONS

Examiner	:	Woo H. Choi
Art Unit	:	2189
Docket No.	:	856063.762
Date	:	January 23, 2007

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT AND

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents:

In response to the Restriction Requirement dated January 4, 2007, Applicants hereby elect Group I, claims 1, 2, 4-9, and 27-33, for examination at this time.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.